



# PRODUCT SNAPSHOT

## PCI Express® Compliant Switches & Bridges

PLX is the World's Largest Provider of PCIe® Switches and Bridges

### PCI Express Switches

ExpressLane™ PCIe Gen 3 and Gen 2 Switches																PCI EXPRESS
PCI-SIG® Base Spec.	Part Number	Lanes	Ports	Latency (ns)	Multi-Root/ Multi-Host	Multi-cast or Dual Cast	Read Pacing	ACS/ ARI*	NT*	DMA*	HPC*	VCs*	SSC*	Power Typ. (W)	Temp. (C°)	Package (mm²)
r3.0	PEX8749	48	18	126	6	MC	Yes	Yes	2	4	3	2	12	7.3	0°+70°	27x27
r3.0	PEX8748	48	12	126	6	MC	Yes	Yes	1	—	3	1	—	7.3	0°+70°	27x27
r3.0	PEX8747	48	5	126	—	MC	Yes	Yes	—	—	0	1	—	7.3	0°+70°	19x21
r3.0	PEX8733	32	18	132	4	MC	Yes	Yes	2	4	3	2	8	6.4	0°+70°	27x27
r3.0	PEX8732	32	8	132	4	MC	Yes	Yes	1	—	3	1	—	5.8	0°+70°	27x27
r3.0	PEX8725	24	10	132	4	MC	Yes	Yes	2	4	3	2	6	5.4	0°+70°	19x19
r3.0	PEX8724	24	6	132	4	MC	Yes	Yes	1	—	3	1	—	5.4	0°+70°	19x19
r3.0	PEX8717	16	10	138	2	MC	Yes	Yes	2	4	3	2	4	4.9	0°+70°	19x19
r3.0	PEX8716	16	4	138	2	MC	Yes	Yes	1	—	3	1	—	4.3	0°+70°	19x19
r3.0	PEX8713	12	10	138	2	MC	Yes	Yes	2	4	3	2	3	4.7	0°+70°	19x19
r3.0	PEX8712	12	3	138	2	MC	Yes	Yes	1	—	3	1	—	4.1	0°+70°	19x19
r2.0	PEX8696	96	24	176	8	MC	Yes	Yes	1	—	4	1	—	10.2	-5°+85°	35x35
r2.0	PEX8680	80	20	176	6	MC	Yes	Yes	1	—	4	1	—	9.0	-5°+85°	35x35
r2.0	PEX8664	64	16	176	5	MC	Yes	Yes	1	—	4	1	—	7.9	-5°+85°	35x35
r2.0	PEX8649	48	12	176	4	MC	Yes	Yes	1	—	3	1	—	6.7	-5°+85°	27x27
r2.0	PEX8648	48	12	140	—	DC	Yes	Yes	1	—	3	1	—	3.5	-40°+85°	27x27
r2.0	PEX8647	48	3	140	—	DC	Yes	Yes	—	—	0	1	—	3.5	-5°+85°	27x27
r2.0	PEX8636	36	24	200	8	MC	Yes	Yes	1	—	4	1	—	8.1	-5°+85°	35x35
r2.0	PEX8632	32	12	145	—	DC	Yes	Yes	1	—	3	1	—	2.9	-40°+85°	27x27
r2.0	PEX8625	24	24	200	8	MC	Yes	Yes	1	—	4	1	—	7.7	-5°+85°	35x35
r2.0	PEX8624	24	6	145	—	DC	Yes	Yes	1	—	3	1	—	2.6	-40°+85°	19x19
r2.0	PEX8619	16	16	140	—	DC	Yes	Yes	1	Yes	^^	2	Yes	2.0	-40°+85°	19x19
r2.0	PEX8618	16	16	140	—	DC	Yes	Yes	1	—	^^	2	Yes	1.9	-5°+85°	19x19
r2.0	PEX8617	16	4	140	—	DC	Yes	Yes	1	—	^^	2	Yes	1.9	-40°+85°	19x19
r2.0	PEX8616	16	4	150	—	DC	Yes	Yes	1	—	2	1	—	2.3	-5°+85°	19x19
r2.0	PEX8615	12	12	140	—	DC	Yes	Yes	1	Yes	^^	2	Yes	1.8	-40°+85°	19x19
r2.0	PEX8614	12	12	140	—	DC	Yes	Yes	1	—	^^	2	Yes	1.7	-5°+85°	19x19
r2.0	PEX8613	12	3	140	—	DC	Yes	Yes	1	—	^^	2	Yes	1.7	-40°+85°	19x19
r2.0	PEX8612	12	3	150	—	DC	Yes	Yes	1	—	2	1	—	2.2	-5°+85°	19x19
r2.0	PEX8609	8	8	140	—	DC	Yes	Yes	1	Yes	^^	2	Yes	1.6	-40°+85°	15x15
r2.0	PEX8608	8	8	140	—	DC	Yes	Yes	1	—	^^	2	Yes	1.4	-5°+85°	15x15
r2.0	PEX8606	6	6	190	—	DC	Yes	Yes	1	—	^^	2	Yes	1.3	-40°+85°	15x15
r2.1	PEX8605	4	4	250	—	—	—	Yes	—	—	—	1	—	0.8	-40°+85°	10x10
r2.0	PEX8604	4	4	190	—	DC	Yes	Yes	1	—	^^	2	Yes	1.3	-40°+85°	15x15
r2.1	PEX8603	3	3	250	—	—	—	Yes	—	—	—	1	—	0.7	-40°+85°	10x10

\*ACS = Access Control Service; ARI = Alternative Routing-ID Interpretation; NT = Non-Transparency; DMA = Direct Memory Access channels; HPC = Hot-Plug Controllers; ^^ = Hot-Plug control via I<sup>2</sup>C; VCs = Virtual Channels; SSC = Spread Spectrum Clock Isolation. Gen 3 devices are fully backwards compatible to Gen 2/Gen 1 and are recommended for all new designs. For PCI Express documentation go to [plxtech.com/pcie](http://plxtech.com/pcie). PCI Express, PCIe, and PCI-SIG are trademarks of the PCI Special Interest Group.

# PLX PCIe PRODUCT SNAPSHOT

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## PCI Express® Bridges

ExpressLane™ PCIe to PCI and PCI-X Bridges										PCI EXPRESS
PCI-SIG® Base Spec.	Part Number	Lanes	Bus Interface	Reverse Mode	Forward Mode	Power	PCI Masters	GPIO	Package Size (mm <sup>2</sup> )	
r1.0a	PEX 8112	1	PCIe to PCI	Yes	Yes	400mW	4	4	10x10 13x13	
r1.0a	PEX 8114	4	PCIe to PCI-X	Yes	Yes	2W	4	0	17x17	

ExpressLane™ PCIe to Local Bus Bridge								PCI EXPRESS
PCI-SIG® Base Spec.	Part Number	Lanes	Description	DMA Channels	Integrated SerDes	Power	Root Complex Mode	Package Size (mm <sup>2</sup> )
r1.0a	PEX 8311	1	8/16/32-bit, 66 MHz	2	Yes	1W	Yes	21x21

PCIe Serial Bridges - UARTs													PCI EXPRESS	
PCI-SIG® Base Spec.	Part Number	UARTs	RS232/RS422/RS485	Max Baud (Mbps)	Data Bits (#)	FIFO Depth (byte)	FIFO Trig Levels	Clock Pre-scaler	Auto XON/XOFF	Auto RTS/CTS	IrDA	Parallel	GPIO	Temp Range
r1.1	OXPCle952	2	Yes	15	5-9	128	Yes	Yes	Yes	Yes	Yes	PP	8	Ind.
r1.1	OXPCle954	4	Yes	15	5-9	128	Yes	Yes	Yes	Yes	Yes	--	8	Ind.
r1.1	OXPCle958	8	Yes	15	5-9	128	Yes	Yes	Yes	Yes	Yes	--	8	Ind.
r1.1	OXPCle840	--										PP	8	Ind.

PCIe to USB and UART Bridge															PCI EXPRESS
PCI-SIG® Base Spec.	Part Number	Dual USB 2.0 Host	USB 2.0 & SPI/SRAM	HS USB 2.0 ULPI Port	FS USB 2.0 Ply	OHCI/EHCI Compliant	USB Suspend/Resume	Internal Memory (kB)	FIFO & Direct Modes	Master & Slave Modes	Max SPI (Mb/s)	Max SRAM (MB/s)	950 UART	GPIO	Temp Range
r1.1	OXPCle200	Yes	Yes	Host	Host	Yes	Yes	8	Yes	Yes	60	25	Yes	8	Ind.

### Other PLX Products:

- 10GBase-T PHY
- NAS SoCs and DAS Controllers
- PCI-to-PCI, & PCI-to-PCI-X Bridges
- PCI-to-Local Bus & Target Bridges
- Serial Bridges - UARTs
- USB Controllers

